

ABSTRACT OF THE DISCLOSURE

This invention relates to a driving circuit for flat panel displays wherein the driving circuit is disposed on a flat panel display panel. The circuit has a plurality of signal lines, at least one buffer unit for inverting a scanning signal, a plurality of switch units and an active area (display area).
5 The plurality of signal lines supplies a plurality of analogous video signals to the plurality of switch units. The unit for inverting a scanning signal generates at least a scanning signal which is then outputted to the plurality of switch units. The analogous video signal so received is transformed into
10 the active-matrix display area by controlling the operation of the plurality of switch units.

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